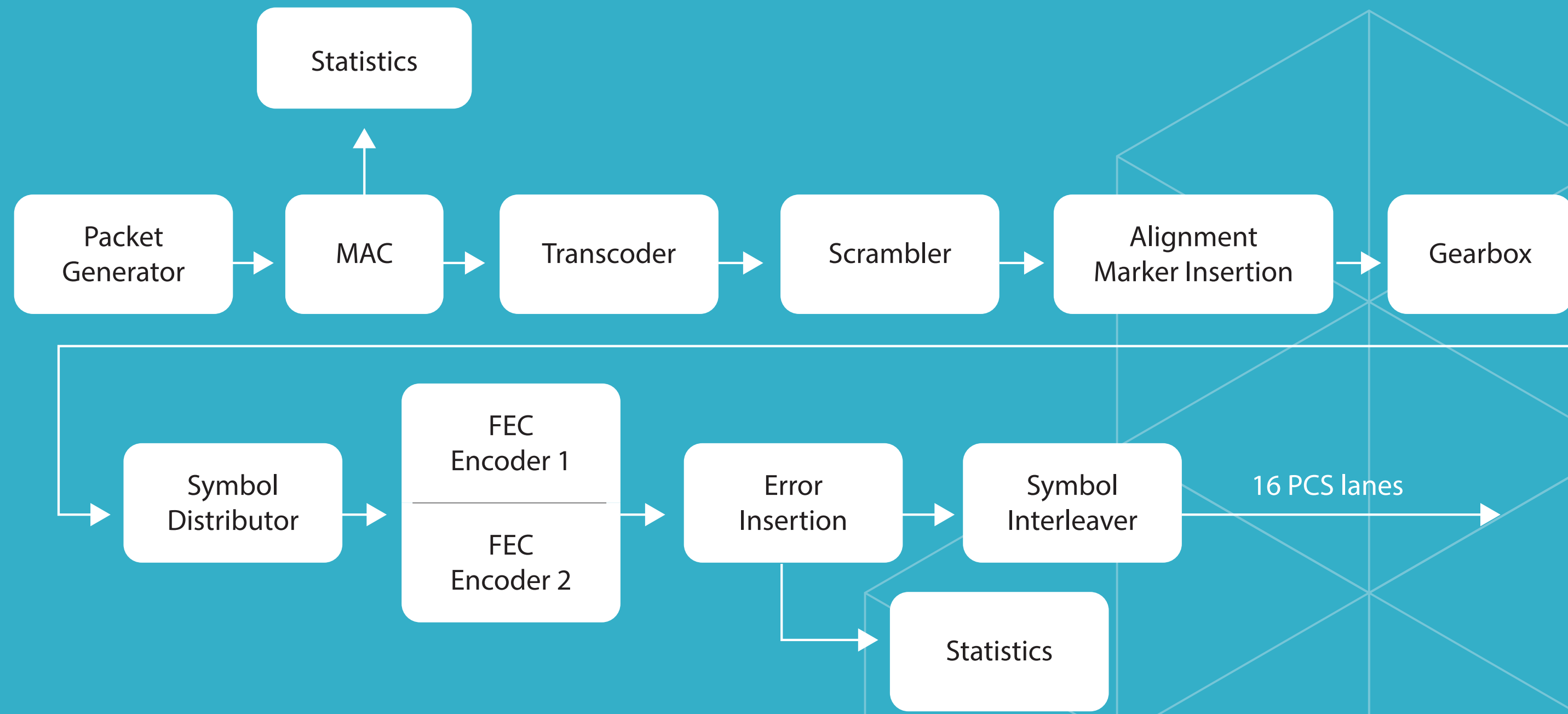




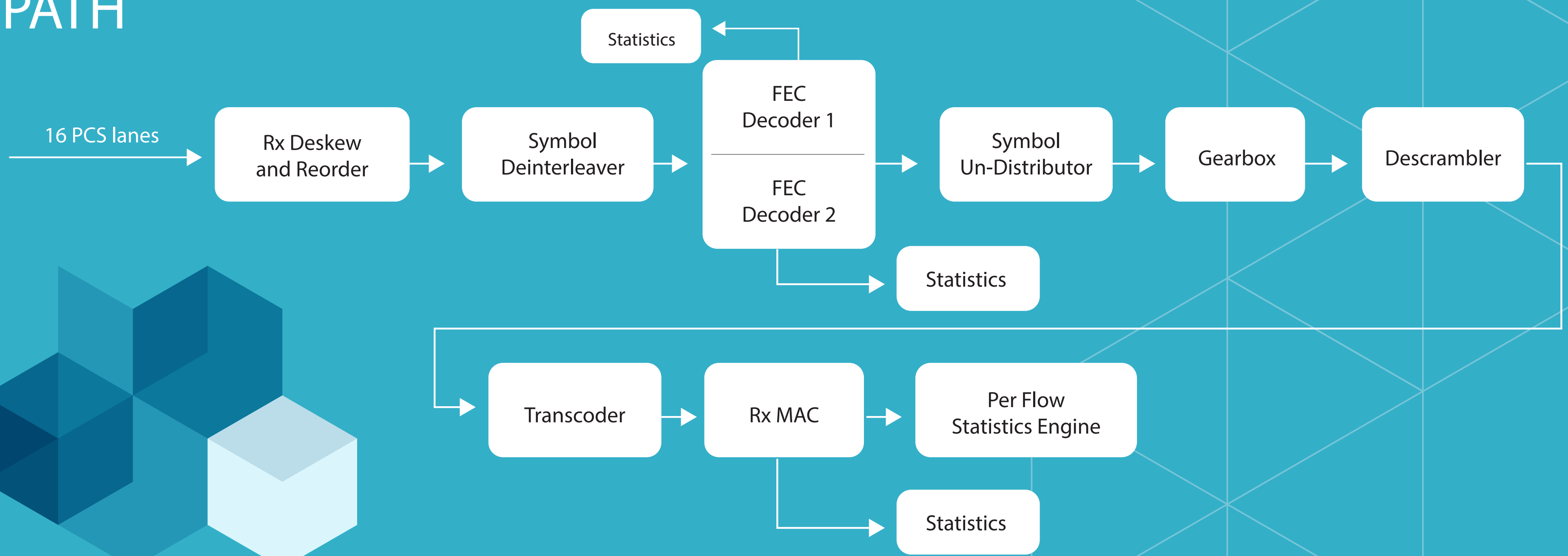
We Make Applications Stronger

Industry's FIRST demonstration of 400GE running at line rate with IEEE Task Force defined Forward Error Correction (FEC) by Ixia

### TRANSMIT PATH



### RECEIVE PATH



- Electrical interface: CDAUI-16
- 400GE line rate traffic
- Comprehensive packet instrumentation
  - Transmit: Timestamp, Sequence number, Flow number
  - Receive: Capture, Per Flow Latency, Per Flow Sequence Checking

- Forward Error Correction (FEC)
  - IEEE 802.3bs Draft 1.3 RS-544
  - Transmit: 2x200G encoder and error insertion
  - Receive: 2x200G decoder able to correct up to 15 symbols per FEC codeword